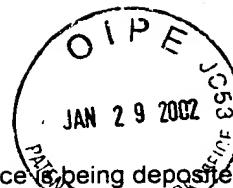


Docket No.: GR 98 P 1513 P



GL/I-815

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By: Mahmud Wolff

Date: December 20, 2001

#(1)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2-20-02
T.E. (2)

Inventor : Holger Hübner et al.
Applic. No. : 09/685,362
Filed : October 10, 2000
Title : Semiconductor Component
Examiner : Matthew E. Warren - Art Unit: 2815

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ORIGINALLY FILED**

RESPONSE

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated November 30, 2001, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-9, for prosecution at this time.

Applicants reserve the right to file a divisional application including the method claims of group II at a later date and/or to request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-9 is solicited.

Respectfully submitted,

MARKUS NOLFF
REG. NO. 37.006

For Applicants

Date: December 20, 2001

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